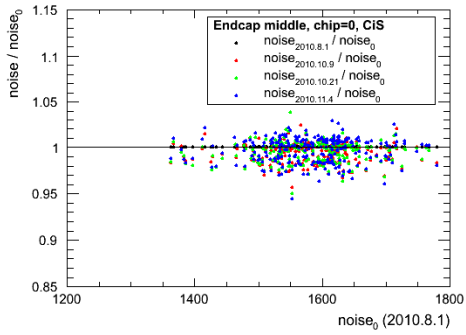


Endcap middle, Cis

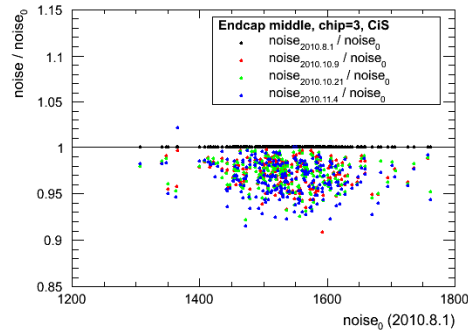
link 0

link 1

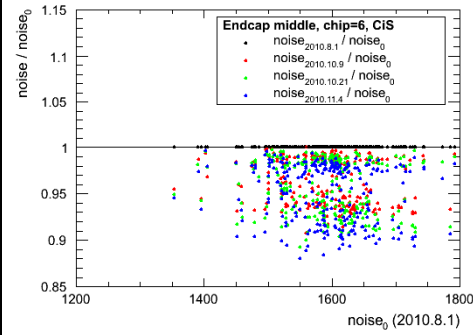
chip 0



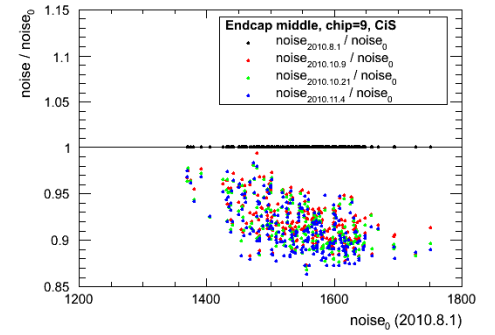
chip 3



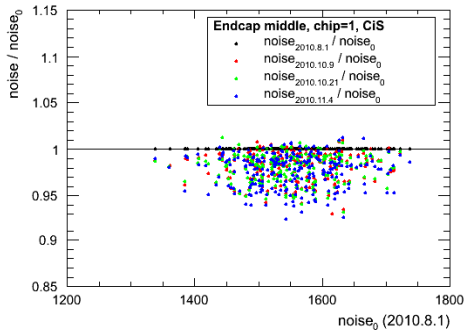
chip 6



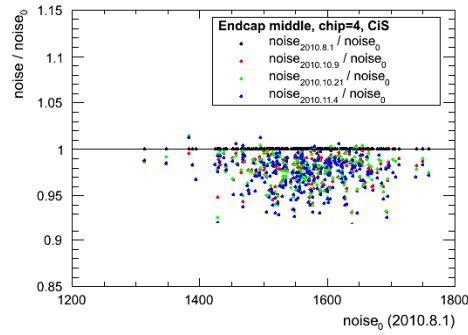
chip 9



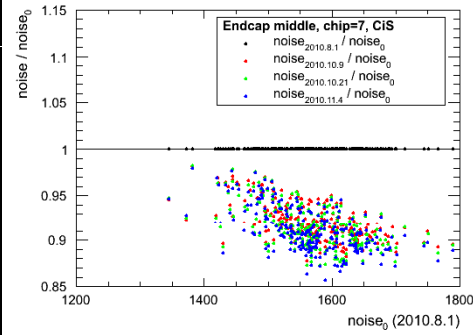
chip 1



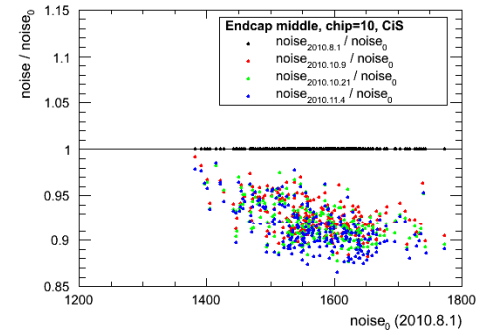
chip 4



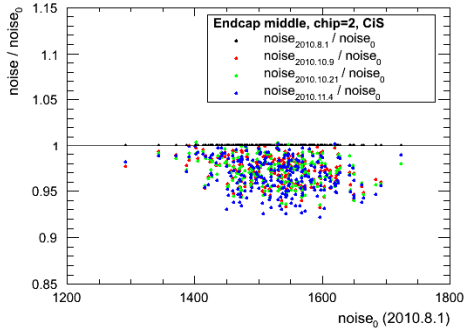
chip 7



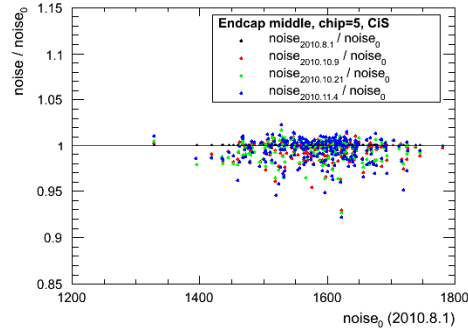
chip 10



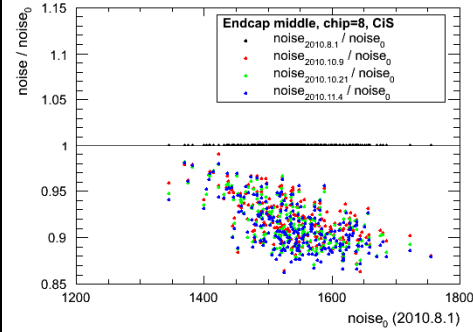
chip 2



chip 5



chip 8



chip 11

