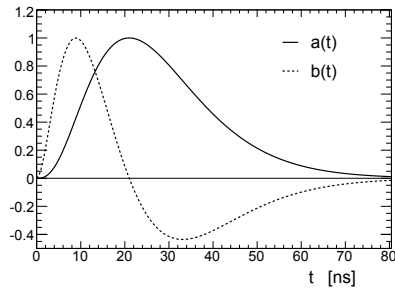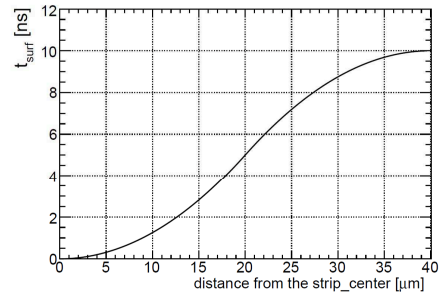This is 2 and 2 test.

Figure 1: Caption of figure 1.

Figure 2: Caption of figure 2.

Figure 1 shows the distribution of average input noise per chip. Figure 2 shows the distribution of average input noise per chip.