AMT-1 Status

for PC Elec Mtg, April 4, 2000 Y. Arai (KEK), yasuo.arai@kek.jp

- AMT-1 chips (ceramic package : 20, plastic package : 200) were delivered to KEK at the end of March.
- All the functional tests done at Toshiba were passed.
- Preliminary User's Manual is available from http://atlas.kek.jp/~araiy/
- New low power LVDS receiver design has been started in collaboration with Toshiba.
- Detailed tests at KEK is being prepared.